Sem-VII E&E

COLLEGEWISE STUDENT RESULT REGISTER FOR VII TH SEMESTER EXAM

(06 ELECTRICAL & ELECTRONIC) ** RC2007-08 **

COLLEGE : GOA COLLEGE OF ENGINEERING — EXAM YEAR : NOV-2014

SEAT NO. / P.R. NO. / ARE CD. / PRO VI. / SEX / NAME OF THE CANDIDATE / PAPER DESCRIPTION	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL		REMARKS
	MAX. 100	25	125	50 OR 25	50 OR 25	MAX.850	
	MIN. 040		50	20 OR 10	20 OR 10	MIN.340	
0358 201203880 (A) N M BHOMKAR SURAJ PRAKASH							
VLSI CIRCUIT DESIGN	062	015	077P		040P		
ELECTRICAL POWER SYSTEM II	044	019	063P				
ADVANCED CONTROLLED DRIVES	078	013	091P				
NEURAL NETWORKS & FUZZY LOGIC	056	015	071P		044P		
FLEXIBLE AC TRANSMISSION SYSTEM	045	010	055P		036P		
PROJECT		022P			044P		
						543	P
0359 201105790 (A) N F BORKAR KIRTI DURGADAS							
VLSI CIRCUIT DESIGN	063	020	083P		043P		
ELECTRICAL POWER SYSTEM II	066	025	091P				
ADVANCED CONTROLLED DRIVES	065	011	076P				
NEURAL NETWORKS & FUZZY LOGIC	065	017	082P		042P		
FLEXIBLE AC TRANSMISSION SYSTEM	066	019	085P		040P		
PROJECT		022P			043P		
						607	P
0360 201104903 (A) N M C SURAJ KUMAR							
VLSI CIRCUIT DESIGN	078	018	096P		038P		
ELECTRICAL POWER SYSTEM II	064	017	081P				
ADVANCED CONTROLLED DRIVES	074	008	082P				
NEURAL NETWORKS & FUZZY LOGIC	079	019	098P		042P		
FLEXIBLE AC TRANSMISSION SYSTEM	060	012	072P		038P		
PROJECT		022P			045P		
						614	P
0361 201105486 (A) N F CHARI DIKSHAV VALLABH							
VLSI CIRCUIT DESIGN	063	018	081P		036P		
ELECTRICAL POWER SYSTEM II	060	019	079P				
ADVANCED CONTROLLED DRIVES	061	005	066P				
NEURAL NETWORKS & FUZZY LOGIC	070	013	083P		040P		
FLEXIBLE AC TRANSMISSION SYSTEM	061	011	072P		040P		
PROJECT		021P			043P		
						561	P
0362 201008378 (A) N M CHARI NEHAL DATTARAM				** RESULT WITHHELD - (SEM. 6 NOT PASSED) **			
VLSI CIRCUIT DESIGN	072	018	090P		040P		
ELECTRICAL POWER SYSTEM II	069	019	088P				
ADVANCED CONTROLLED DRIVES	072	008	080P				
NEURAL NETWORKS & FUZZY LOGIC	074	016	090P		044P		
FLEXIBLE AC TRANSMISSION SYSTEM	054	013	067P		042P		
PROJECT		023P			046P		

P : PASSES; F : FAILS; A : ABSENT

[06 ELECTRICAL & ELECTRONIC] I
COLLEGE : GOA COLLEGE OF ENGINEERING
** RC2007-08 **
EXAM YEAR : NOV-2014

SEAT NO. / P.R. NO. / ARE / PRO CD. / SEX / NAME OF THE CANDIDATE / PAPER DESCRIPTION	THEORY		SESSIONAL	TOTAL	PRACTICAL	ORAL		REMARKS
	MAX. 100		25	125	50 OR 25	50 OR 25	MAX.850	
	MIN. 040			50	20 OR 10	20 OR 10	MIN.340	
0363 201105487 (A) N M CHAUBEY MANISH KUMAR					** RESULT WITHHELD - (SEM. 5 NOT PASSED) **			
VLSI CIRCUIT DESIGN	035	$5	010	045E	$5	030E		
ELECTRICAL POWER SYSTEM II	055		013	068E				
ADVANCED CONTROLLED DRIVES	049		005	054E				
NEURAL NETWORKS & FUZZY LOGIC	032		010	042F		030E		
FLEXIBLE AC TRANSMISSION SYSTEM	039	$1	010	049E	$1	034E		
PROJECT			019E			045E		
0364 201105489 (A) N M DEODHAR NIKHIL SUHAS								
VLSI CIRCUIT DESIGN	070		019	089P		048P		
ELECTRICAL POWER SYSTEM II	064		019	083P				
ADVANCED CONTROLLED DRIVES	042		012	054P				
NEURAL NETWORKS & FUZZY LOGIC	056		017	073P		048P		
FLEXIBLE AC TRANSMISSION SYSTEM	044		018	062P		043P		
PROJECT			023P			046P		
							569	P
0365 201105490 (A) N M DEV KAPIL HEMANT								
VLSI CIRCUIT DESIGN	040		014	054E		033E		
ELECTRICAL POWER SYSTEM II	041		010	051E				
ADVANCED CONTROLLED DRIVES	053		002	055E				
NEURAL NETWORKS & FUZZY LOGIC	025		010	035F		034E		
FLEXIBLE AC TRANSMISSION SYSTEM	040		010	050E		036E		
PROJECT			023E			043E		
							414	F
0366 201104649 (A) N M DHAVJEKAR TUKARAM ALIAS SAURABH PRADIP								
VLSI CIRCUIT DESIGN	043		019	082P		035P		
ELECTRICAL POWER SYSTEM II	065		018	083P				
ADVANCED CONTROLLED DRIVES	070		006	076P				
NEURAL NETWORKS & FUZZY LOGIC	070		015	085P		040P		
FLEXIBLE AC TRANSMISSION SYSTEM	060		010	070P		040P		
PROJECT			022P			042P		
							575	P
0367 201006319 (A) N M DOURADO RAINER JULIAN					** RESULT WITHHELD - (SEM. 5 NOT PASSED) **			
VLSI CIRCUIT DESIGN	021		002	023F		020E		
ELECTRICAL POWER SYSTEM II	040		014	054E				
ADVANCED CONTROLLED DRIVES	000		002	002A				
NEURAL NETWORKS & FUZZY LOGIC	015		007	022F		020E		
FLEXIBLE AC TRANSMISSION SYSTEM	014		005	019F		025E		
PROJECT			015E			025E		

P : PASSES; F : FAILS; A : ABSENT

[06 ELECTRICAL & ELECTRONIC] ** RC2007-08 **
COLLEGE : GOA COLLEGE OF ENGINEERING EXAM YEAR : NOV-2014

SEAT NO.	P.R. NO. ARE CD. VI. PAPER DESCRIPTION		THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL		REMARKS
		MAX.	100	25	125	50 OR 25	50 OR 25	MAX.850	
		MIN.	040		50	20 OR 10	20 OR 10	MIN.340	
0368	201105492 (A) N M FERNANDES AUSHBEN AGNELO								
	VLSI CIRCUIT DESIGN		063	020	083P		038P		
	ELECTRICAL POWER SYSTEM II		085	019	104P				
	ADVANCED CONTROLLED DRIVES		079	008	087P				
	NEURAL NETWORKS & FUZZY LOGIC		050	016	066P		038P		
	FLEXIBLE AC TRANSMISSION SYSTEM		043	013	056P		038P		
	PROJECT			022P			046P		
								578	P
0369	201105493 (A) N M FERNANDES MEBLISH								
	VLSI CIRCUIT DESIGN		072	017	089P		041P		
	ELECTRICAL POWER SYSTEM II		058	021	079P				
	ADVANCED CONTROLLED DRIVES		083	006	089P				
	NEURAL NETWORKS & FUZZY LOGIC		053	017	070P		045P		
	FLEXIBLE AC TRANSMISSION SYSTEM		044	010	054P		043P		
	PROJECT			023P			047P		
								580	P
0370	201203891 (A) N M FURTADO JAISON JOSEINAS								
	VLSI CIRCUIT DESIGN		041	017	058P		041P		
	ELECTRICAL POWER SYSTEM II		063	017	080P				
	ADVANCED CONTROLLED DRIVES		075	010	085P				
	NEURAL NETWORKS & FUZZY LOGIC		060	015	075P		042P		
	FLEXIBLE AC TRANSMISSION SYSTEM		062	012	074P		037P		
	PROJECT			024P			045P		
								561	P
0371	201203893 (A) N M G ANIL KUMAR								
	VLSI CIRCUIT DESIGN		040	013	053P		033P		
	ELECTRICAL POWER SYSTEM II		054	020	074P				
	ADVANCED CONTROLLED DRIVES		070	002	072P				
	NEURAL NETWORKS & FUZZY LOGIC		040	010	050P		036P		
	FLEXIBLE AC TRANSMISSION SYSTEM		036 $4	013	049P $4		040P		
	PROJECT			020P			040P		
								467 $04	P
0372	201008365 (A) N M GAIKWAD RAMESH DARSHAN								
	VLSI CIRCUIT DESIGN		085	018	103P		042P		
	ELECTRICAL POWER SYSTEM II		065	016	081P				
	ADVANCED CONTROLLED DRIVES		065	005	070P				
	NEURAL NETWORKS & FUZZY LOGIC		080	020	100P		045P		
	FLEXIBLE AC TRANSMISSION SYSTEM		068	019	087P		040P		
	PROJECT			022P			043P		
								633	P

P : PASSES; F : FAILS; A : ABSENT

[04 ELECTRICAL & ELECTRONIC] ** RC2007-08 **
COLLEGE : GOA COLLEGE OF ENGINEERING EXAM YEAR : NOV-2014

SEAT NO.	P.R. NO. / PAPER DESCRIPTION	ARE CD.	PRO VI.	SEX	NAME OF THE CANDIDATE	THEORY MAX. 100 MIN. 040	SESSIONAL 25	TOTAL 125 50	PRACTICAL 50 OR 25 20 OR 10	ORAL 50 OR 25 20 OR 10	REMARKS MAX.850 MIN.340
0373	201105494	(A)	N	F	GAONKAR POOJA TRIVIKRAM						
	VLSI CIRCUIT DESIGN					040	014	054P		038P	
	ELECTRICAL POWER SYSTEM II					063	016	079P			
	ADVANCED CONTROLLED DRIVES					041	004	045P	$5		
	NEURAL NETWORKS & FUZZY LOGIC					043	015	058P		042P	
	FLEXIBLE AC TRANSMISSION SYSTEM					046	011	057P		038P	
	PROJECT						022P			045P	
										478	$05 P
0374	201203896	(A)	N	M	GAONKAR PRAVEEN RAMESH						
	VLSI CIRCUIT DESIGN					052	012	064P		030P	
	ELECTRICAL POWER SYSTEM II					068	016	084P			
	ADVANCED CONTROLLED DRIVES					080	012	092P			
	NEURAL NETWORKS & FUZZY LOGIC					043	012	055P		042P	
	FLEXIBLE AC TRANSMISSION SYSTEM					053	011	064P		036P	
	PROJECT						022P			045P	
										534	P
0375	201105495	(A)	N	F	GAUDE KHUSHBOO DATTARAM						
	VLSI CIRCUIT DESIGN					069	018	087P		041P	
	ELECTRICAL POWER SYSTEM II					064	019	083P			
	ADVANCED CONTROLLED DRIVES					070	008	078P			
	NEURAL NETWORKS & FUZZY LOGIC					062	019	081P		044P	
	FLEXIBLE AC TRANSMISSION SYSTEM					048	014	062P		035P	
	PROJECT						020P			040P	
										571	P
0376	201105497	(A)	N	M	GAUNS ANURAJ DAMODAR	** RESULT WITHHELD - (SEM. 6 NOT PASSED) **					
	VLSI CIRCUIT DESIGN					060	016	076P		036P	
	ELECTRICAL POWER SYSTEM II					058	015	073P			
	ADVANCED CONTROLLED DRIVES					065	008	073P			
	NEURAL NETWORKS & FUZZY LOGIC					058	017	075P		038P	
	FLEXIBLE AC TRANSMISSION SYSTEM					052	010	062P		036P	
	PROJECT						016P			032P	
0377	201105262	(A)	N	M	GAUNS GAONKAR RUTURAJ ULHAS						
	VLSI CIRCUIT DESIGN					062	016	078P		041P	
	ELECTRICAL POWER SYSTEM II					058	017	075P			
	ADVANCED CONTROLLED DRIVES					058	008	066P			
	NEURAL NETWORKS & FUZZY LOGIC					061	019	080P		040P	
	FLEXIBLE AC TRANSMISSION SYSTEM					055	010	065P		036P	
	PROJECT						020P			045P	
										546 #09	P

P : PASSES; F : FAILS; A : ABSENT

[06 ELECTRICAL & ELECTRONIC] ** RC2007-08 **
COLLEGE : GOA COLLEGE OF ENGINEERING EXAM YEAR : NOV-2014

SEAT NO. / P.R. NO. / ARE CD. / PRO VI. / SEX / NAME OF THE CANDIDATE / PAPER DESCRIPTION	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
	MAX. 100	25	125	50 OR 25	50 OR 25	MAX.650
	MIN. 040		50	20 OR 10	20 OR 10	MIN.340
0378 201105498 (A) N M GAUNS SHUBHAM ALIAS TANAJI SHAMBA						
VLSI CIRCUIT DESIGN	074	017	091P		040P	
ELECTRICAL POWER SYSTEM II	075	021	096P			
ADVANCED CONTROLLED DRIVES	092	014	106P			
NEURAL NETWORKS & FUZZY LOGIC	077	019	096P		045P	
FLEXIBLE AC TRANSMISSION SYSTEM	073	020	093P		044P	
PROJECT		022P			042P	
						675 #09 P
0379 200703683 (A) N M GOMES MAXSON				** RESULT WITHHELD - (SEM. 5 NOT PASSED) **		
VLSI CIRCUIT DESIGN	000	000	000A		000A	
ELECTRICAL POWER SYSTEM II	000	000	000A			
ADVANCED CONTROLLED DRIVES	000	000	000A			
NEURAL NETWORKS & FUZZY LOGIC	000	000	000A		000A	
FLEXIBLE AC TRANSMISSION SYSTEM	000	000	000A		000A	
PROJECT		000A			000A	
0380 201203899 (A) N M GOSWAMI DHAVALBHARTI MAHESHBHARTI						
VLSI CIRCUIT DESIGN	057	020	077P		042P	
ELECTRICAL POWER SYSTEM II	063	024	087P			
ADVANCED CONTROLLED DRIVES	062	013	075P			
NEURAL NETWORKS & FUZZY LOGIC	051	021	072P		046P	
FLEXIBLE AC TRANSMISSION SYSTEM	049	017	066P		038P	
PROJECT		023P			046P	
						572 P
0381 201203902 (A) N F GURAV POOJA KALLAPPA				** RESULT WITHHELD - (SEM. 6 NOT PASSED) **		
VLSI CIRCUIT DESIGN	049	010	059P		032P	
ELECTRICAL POWER SYSTEM II	076	020	096P			
ADVANCED CONTROLLED DRIVES	051	007	058P			
NEURAL NETWORKS & FUZZY LOGIC	068	019	087P		042P	
FLEXIBLE AC TRANSMISSION SYSTEM	044	010	054P		038P	
PROJECT		019P			044P	
0382 201105529 (A) N M HARMALKAR RAHUL GURUDAS						
VLSI CIRCUIT DESIGN	088	024	112P		045P	
ELECTRICAL POWER SYSTEM II	073	025	098P			
ADVANCED CONTROLLED DRIVES	083	016	099P			
NEURAL NETWORKS & FUZZY LOGIC	067	016	083P		040P	
FLEXIBLE AC TRANSMISSION SYSTEM	059	012	071P		035P	
PROJECT		023P			044P	
						650 P

P : PASSES; F : FAILS; A : ABSENT

[06 ELECTRICAL & ELECTRONIC] ** RC2007-08 **
COLLEGE : GOA COLLEGE OF ENGINEERING EXAM YEAR : NOV-2014

SEAT NO. / P.R. NO. / ARE / PRO / SEX / NAME OF THE CANDIDATE / PAPER DESCRIPTION	THEORY MAX. 100 MIN. 040	SESSIONAL 25	TOTAL 125 50	PRACTICAL 50 OR 25 20 OR 10	ORAL 50 OR 25 20 OR 10	MAX.850 MIN.340	REMARKS
0383 201105268 (A) N M HEMANT SURESH			** SEM. 6 RESULT NOT DECLARED **				
VLSI CIRCUIT DESIGN	042	013	055E		035E		
ELECTRICAL POWER SYSTEM II	052	016	068E				
ADVANCED CONTROLLED DRIVES	055	001	056E				
NEURAL NETWORKS & FUZZY LOGIC	050	013	063E		038E		
FLEXIBLE AC TRANSMISSION SYSTEM	024	010	034F		037E		
PROJECT		020E			045E		
0384 201105825 (A) N M HIJAM MARTIN LUWANG			** RESULT WITHHELD - (SEM. 6 NOT PASSED) **				
VLSI CIRCUIT DESIGN	035	005	040F		000A		
ELECTRICAL POWER SYSTEM II	052	002	054E				
ADVANCED CONTROLLED DRIVES	067	000	067E				
NEURAL NETWORKS & FUZZY LOGIC	063	000	063E		000A		
FLEXIBLE AC TRANSMISSION SYSTEM	007	000	007F		000A		
PROJECT		016E			033E		
0385 201105442 (A) N M KAMBLI ASHVEK RAMESH							
VLSI CIRCUIT DESIGN	074	020	094P		039P		
ELECTRICAL POWER SYSTEM II	059	019	078P				
ADVANCED CONTROLLED DRIVES	071	010	081P				
NEURAL NETWORKS & FUZZY LOGIC	082	018	100P		042P		
FLEXIBLE AC TRANSMISSION SYSTEM	063	021	084P		042P		
PROJECT		022P			043P		
						625	P
0386 201105532 (A) N M KHAN SAHIL							
VLSI CIRCUIT DESIGN	043	011	054P		039P		
ELECTRICAL POWER SYSTEM II	056	017	073P				
ADVANCED CONTROLLED DRIVES	070	003	073P				
NEURAL NETWORKS & FUZZY LOGIC	050	014	064P		038P		
FLEXIBLE AC TRANSMISSION SYSTEM	055	010	065P		035P		
PROJECT		021P			041P		
						503	P
0387 201105533 (A) N M KHANDOLKAR SANMESH SITARAM							
VLSI CIRCUIT DESIGN	048	013	061P		035P		
ELECTRICAL POWER SYSTEM II	048	018	066P				
ADVANCED CONTROLLED DRIVES	059	008	067P				
NEURAL NETWORKS & FUZZY LOGIC	051	014	065P		038P		
FLEXIBLE AC TRANSMISSION SYSTEM	060	012	072P		035P		
PROJECT		015P			032P		
						486	P

P : PASSES; F : FAILS; A : ABSENT

COLLEGEWISE STUDENT RESULT REGISTER FOR VII TH SEMESTER EXAM

[06 ELECTRICAL & ELECTRONIC : 1 — ** RC2007-08 **

COLLEGE : GOA COLLEGE OF ENGINEERING — EXAM YEAR : NOV-2014

SEAT NO.	P.R. NO. ARE CD. VI. / PAPER DESCRIPTION	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL		REMARKS
		MAX. 100	25	125	50 OR 25	50 OR 25	MAX.850	
		MIN. 040		50	20 OR 10	20 OR 10	MIN.340	
0388	201105536 (A) N M KHEDEKAR VIBHAV GURUDAS							
	VLSI CIRCUIT DESIGN	080	022	102P		047P		
	ELECTRICAL POWER SYSTEM II	050	021	071P				
	ADVANCED CONTROLLED DRIVES	077	005	082P				
	NEURAL NETWORKS & FUZZY LOGIC	070	022	092P		045P		
	FLEXIBLE AC TRANSMISSION SYSTEM	060	012	072P		040P		
	PROJECT		024P			048P		
							623	P
0389	201105538 (A) N M KOMARPANT MANDAR MAHADEV							
	VLSI CIRCUIT DESIGN	066	010	076P		038P		
	ELECTRICAL POWER SYSTEM II	055	010	065P				
	ADVANCED CONTROLLED DRIVES	058	003	061P				
	NEURAL NETWORKS & FUZZY LOGIC	049	010	059P		035P		
	FLEXIBLE AC TRANSMISSION SYSTEM	040	011	051P		038P		
	PROJECT		019P			038P		
							480	P
0390	201105539 (A) N M KORDE CHARUDATTA GURUDAS							
	VLSI CIRCUIT DESIGN	066	021	087P		044P		
	ELECTRICAL POWER SYSTEM II	064	021	085P				
	ADVANCED CONTROLLED DRIVES	065	011	076P				
	NEURAL NETWORKS & FUZZY LOGIC	076	025	101P		046P		
	FLEXIBLE AC TRANSMISSION SYSTEM	058	015	073P		038P		
	PROJECT		024P			048P		
							622	P
0391	201105541 (A) N M LUIS LEE MCKENZIE							
	VLSI CIRCUIT DESIGN	057	014	071P		044P		
	ELECTRICAL POWER SYSTEM II	072	019	091P				
	ADVANCED CONTROLLED DRIVES	074	011	085P				
	NEURAL NETWORKS & FUZZY LOGIC	055	017	072P		043P		
	FLEXIBLE AC TRANSMISSION SYSTEM	062	015	077P		042P		
	PROJECT		024P			047P		
							596	P
0392	201104676 (A) N F M. SEETHALAXMI							
	VLSI CIRCUIT DESIGN	069	019	088P		039P		
	ELECTRICAL POWER SYSTEM II	054	020	074P				
	ADVANCED CONTROLLED DRIVES	077	010	087P				
	NEURAL NETWORKS & FUZZY LOGIC	060	015	075P		042P		
	FLEXIBLE AC TRANSMISSION SYSTEM	052	014	066P		038P		
	PROJECT		020P			045P		
							574	P

P : PASSES; F : FAILS; A : ABSENT

[06 ELECTRICAL & ELECTRONIC 1 — ** RC2007-08 **
COLLEGE : GOA COLLEGE OF ENGINEERING — EXAM YEAR : NOV-2014

SEAT NO.	P.R. NO. ARE CD. VI. / PAPER DESCRIPTION	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL		REMARKS
		MAX. 100	25	125	50 OR 25	50 OR 25	MAX.850	
		MIN. 040		50	20 OR 10	20 OR 10	MIN.340	
0393	201004597 (A) N M MALANJA KHURAIJAM					** RESULT WITHHELD - (SEM. 5 NOT PASSED) **		
	VLSI CIRCUIT DESIGN	000	010	010A		000A		
	ELECTRICAL POWER SYSTEM II	042	009	051E				
	ADVANCED CONTROLLED DRIVES	000	003	003A				
	NEURAL NETWORKS & FUZZY LOGIC	040	000	040F		000A		
	FLEXIBLE AC TRANSMISSION SYSTEM	040	000	040F		000A		
	PROJECT		016E			032E		
0394	201105542 (A) N M MATHEW ANAND							
	VLSI CIRCUIT DESIGN	071	019	090P		042P		
	ELECTRICAL POWER SYSTEM II	072	024	096P				
	ADVANCED CONTROLLED DRIVES	088	015	103P				
	NEURAL NETWORKS & FUZZY LOGIC	087	022	109P		044P		
	FLEXIBLE AC TRANSMISSION SYSTEM	057	019	076P		040P		
	PROJECT		022P			042P		
							664	P
0395	201105482 (A) N M MESQUITA ANTHONY							
	VLSI CIRCUIT DESIGN	054	015	069P		043P		
	ELECTRICAL POWER SYSTEM II	065	017	082P				
	ADVANCED CONTROLLED DRIVES	067	011	078P				
	NEURAL NETWORKS & FUZZY LOGIC	067	014	081P		038P		
	FLEXIBLE AC TRANSMISSION SYSTEM	044	005	049P	@1	035P		
	PROJECT		022P			043P		
							540 @01	P
0396	201105547 (A) N M NAIK GAUNKER PARASH MOHAN							
	VLSI CIRCUIT DESIGN	044	015	059P		041P		
	ELECTRICAL POWER SYSTEM II	056	020	076P				
	ADVANCED CONTROLLED DRIVES	078	008	086P				
	NEURAL NETWORKS & FUZZY LOGIC	071	014	085P		044P		
	FLEXIBLE AC TRANSMISSION SYSTEM	052	018	070P		037P		
	PROJECT		023P			043P		
							564	P
0397	201105549 (A) N M NAIK KRISHNAN VASSANT							
	VLSI CIRCUIT DESIGN	053	011	064P		040P		
	ELECTRICAL POWER SYSTEM II	061	016	077P				
	ADVANCED CONTROLLED DRIVES	073	005	078P				
	NEURAL NETWORKS & FUZZY LOGIC	060	010	070P		035P		
	FLEXIBLE AC TRANSMISSION SYSTEM	050	012	062P		040P		
	PROJECT		016P			031P		
							513	P

[06 ELECTRICAL & ELECTRONIC] ** RC2007-08 **
COLLEGE : GOA COLLEGE OF ENGINEERING EXAM YEAR : NOV-2014

SEAT NO. / P.R. NO. / ARE / PRO CD. / SEX / PAPER DESCRIPTION	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
	MAX. 100	25	125	50 OR 25	50 OR 25	MAX.850
	MIN. 040		50	20 OR 10	20 OR 10	MIN.340
0398 20110555 (A) N M NAYAK AMEY CHANDRASHEKHAR						
VLSI CIRCUIT DESIGN	067	017	084P		044P	
ELECTRICAL POWER SYSTEM II	064	013	077P			
ADVANCED CONTROLLED DRIVES	069	006	075P			
NEURAL NETWORKS & FUZZY LOGIC	061	015	076P		042P	
FLEXIBLE AC TRANSMISSION SYSTEM	048	010	058P		040P	
PROJECT		024P			047P	
					567	P
0399 20110530 1 (A) N M PARAB SANTOSH SADANAND				** RESULT WITHHELD - (SEM. 6 NOT PASSED) **		
VLSI CIRCUIT DESIGN	062	017	079P		039P	
ELECTRICAL POWER SYSTEM II	051	018	069P			
ADVANCED CONTROLLED DRIVES	057	005	062P			
NEURAL NETWORKS & FUZZY LOGIC	062	020	082P		044P	
FLEXIBLE AC TRANSMISSION SYSTEM	057	017	074P		040P	
PROJECT		020P			045P	
0400 20110484 3 (A) N M PARANJAPE SHRIKANT SUDEV						
VLSI CIRCUIT DESIGN	046	015	061P		037P	
ELECTRICAL POWER SYSTEM II	050	022	072P			
ADVANCED CONTROLLED DRIVES	064	009	073P			
NEURAL NETWORKS & FUZZY LOGIC	050	013	063P		044P	
FLEXIBLE AC TRANSMISSION SYSTEM	056	012	068P		037P	
PROJECT		022P			044P	
					521	P
0401 20110555 8 (A) N F PARODKAR MANJUSHA RAJENDRA						
VLSI CIRCUIT DESIGN	080	021	101P		045P	
ELECTRICAL POWER SYSTEM II	056	023	079P			
ADVANCED CONTROLLED DRIVES	076	006	082P			
NEURAL NETWORKS & FUZZY LOGIC	076	021	097P		044P	
FLEXIBLE AC TRANSMISSION SYSTEM	051	013	064P		037P	
PROJECT		022P			046P	
					617	P
0402 20110555 9 (A) N M PATIL VIRAJ VISHWAS						
VLSI CIRCUIT DESIGN	068	014	082P		040P	
ELECTRICAL POWER SYSTEM II	063	015	078P			
ADVANCED CONTROLLED DRIVES	090	011	101P			
NEURAL NETWORKS & FUZZY LOGIC	066	010	076P		038P	
FLEXIBLE AC TRANSMISSION SYSTEM	069	012	081P		036P	
PROJECT		023P			043P	
					598 #09	P

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SEAT NO. / P.R. NO. / PAPER CD. / DESCRIPTION	ARE	PRO	SEX	NAME OF THE CANDIDATE	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
					MAX. 100	25	125	50 OR 25	50 OR 25	MAX.850
					MIN. 040		50	20 OR 10	20 OR 10	MIN.340
0403 201105306	(A)	N	M	PHADTE NARAYAN SANJAY					** RESULT WITHHELD - (SEM. 6 NOT PASSED) **	
VI	VLSI CIRCUIT DESIGN				031	019	050F		043E	
VI	ELECTRICAL POWER SYSTEM II				048	016	064E			
VI	ADVANCED CONTROLLED DRIVES				056	006	062E			
VI	NEURAL NETWORKS & FUZZY LOGIC				041	012	053E		038E	
VI	FLEXIBLE AC TRANSMISSION SYSTEM				046	010	056E		036E	
VI	PROJECT					021E			040E	
0404 201105789	(A)	N	M	PINGALE LAXMIKANT VITHOBA						
VI	VLSI CIRCUIT DESIGN				087	022	109P		040P	
VI	ELECTRICAL POWER SYSTEM II				071	019	090P			
VI	ADVANCED CONTROLLED DRIVES				080	012	092P			
VI	NEURAL NETWORKS & FUZZY LOGIC				087	017	104P		040P	
VI	FLEXIBLE AC TRANSMISSION SYSTEM				069	020	089P		040P	
VI	PROJECT					021P			040P	
									665	P
0405 201203906	(A)	N	M	POJARY SACHIN SADANAND						
VI	VLSI CIRCUIT DESIGN				061	018	079P		041P	
VI	ELECTRICAL POWER SYSTEM II				055	019	074P			
VI	ADVANCED CONTROLLED DRIVES				065	010	075P			
VI	NEURAL NETWORKS & FUZZY LOGIC				047	012	059P		042P	
VI	FLEXIBLE AC TRANSMISSION SYSTEM				060	010	070P		037P	
VI	PROJECT					024P			045P	
									546	P
0406 201105561	(A)	N	M	PRABHU DESSAI SHUBHAM JAIPRAKASH						
VI	VLSI CIRCUIT DESIGN				062	023	085P		045P	
VI	ELECTRICAL POWER SYSTEM II				063	022	085P			
VI	ADVANCED CONTROLLED DRIVES				081	009	090P			
VI	NEURAL NETWORKS & FUZZY LOGIC				050	019	069P		040P	
VI	FLEXIBLE AC TRANSMISSION SYSTEM				058	015	073P		038P	
VI	PROJECT					022P			042P	
									589 #09	P
0407 201105562	(A)	N	M	PRABHU VAIBHAV PRADEEP						
VI	VLSI CIRCUIT DESIGN				080	019	099P		046P	
VI	ELECTRICAL POWER SYSTEM II				057	021	078P			
VI	ADVANCED CONTROLLED DRIVES				069	011	080P			
VI	NEURAL NETWORKS & FUZZY LOGIC				065	025	090P		044P	
VI	FLEXIBLE AC TRANSMISSION SYSTEM				060	017	077P		044P	
VI	PROJECT					023P			044P	
									625	P

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COLLEGE : GOA COLLEGE OF ENGINEERING EXAM YEAR : NOV-2014

SEAT NO. / P.R. NO. / ARE / PRO CD. / SEX / NAME OF THE CANDIDATE / PAPER DESCRIPTION	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL		REMARKS
	MAX. 100	25	125	50 OR 25	50 OR 25	MAX.850	
	MIN. 040		50	20 OR 10	20 OR 10	MIN.340	
0408 201104894 (A) N M RAJAT KUMAR SINGH CHAUHAN							
VLSI CIRCUIT DESIGN	086	024	110P		048P		
ELECTRICAL POWER SYSTEM II	077	022	099P				
ADVANCED CONTROLLED DRIVES	085	010	095P				
NEURAL NETWORKS & FUZZY LOGIC	089	023	112P		046P		
FLEXIBLE AC TRANSMISSION SYSTEM	058	020	078P		045P		
PROJECT		023P			043P		
						699	P
0409 201105564 (A) N M RAJPUT SHRINEEL SATISH							
VLSI CIRCUIT DESIGN	057	018	075P		041P		
ELECTRICAL POWER SYSTEM II	060	021	081P				
ADVANCED CONTROLLED DRIVES	070	011	081P				
NEURAL NETWORKS & FUZZY LOGIC	060	012	072P		038P		
FLEXIBLE AC TRANSMISSION SYSTEM	049	010	059P		040P		
PROJECT		022P			042P		
						551	P
0410 201104901 (A) N M RAKESH NYANDEV KHOTH							
VLSI CIRCUIT DESIGN	058	016	074P		030P		
ELECTRICAL POWER SYSTEM II	073	016	089P				
ADVANCED CONTROLLED DRIVES	075	006	081P				
NEURAL NETWORKS & FUZZY LOGIC	035 $5	013	048P $5		038P		
FLEXIBLE AC TRANSMISSION SYSTEM	052	010	062P		038P		
PROJECT		021P			044P		
						525 $05	P
0411 201105565 (A) N M RANE KARAN PARSHURAM							
VLSI CIRCUIT DESIGN	078	022	100P		047P		
ELECTRICAL POWER SYSTEM II	056	023	079P				
ADVANCED CONTROLLED DRIVES	076	023	099P				
NEURAL NETWORKS & FUZZY LOGIC	082	025	107P		047P		
FLEXIBLE AC TRANSMISSION SYSTEM	065	020	085P		045P		
PROJECT		024P			047P		
						680	P
0412 201203907 (A) N M RAUT DESSAI SHIDHRAJ BHARAT					** RESULT WITHHELD - (SEM. 6 NOT PASSED) **		
VLSI CIRCUIT DESIGN	035 $5	014	049E $5		036E		
ELECTRICAL POWER SYSTEM II	030	013	043F				
ADVANCED CONTROLLED DRIVES	051	004	055E				
NEURAL NETWORKS & FUZZY LOGIC	047	014	061E		035E		
FLEXIBLE AC TRANSMISSION SYSTEM	041	011	052E		040E		
PROJECT		022E			045E		

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SEAT NO. / P.R. NO. / ARE CD. / PRO VI. / SEX / NAME OF THE CANDIDATE / PAPER DESCRIPTION	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL	REMARKS
	MAX. 100	25	125	50 OR 25	50 OR 25	MAX.850
	MIN. 040		50	20 OR 10	20 OR 10	MIN.340
0413 201105568 (A) N M SANCOLCAR SAYISH ASHOK						
VLSI CIRCUIT DESIGN	068	012	080P		038P	
ELECTRICAL POWER SYSTEM II	042	015	057P			
ADVANCED CONTROLLED DRIVES	079	008	087P			
NEURAL NETWORKS & FUZZY LOGIC	050	012	062P		038P	
FLEXIBLE AC TRANSMISSION SYSTEM	056	010	066P		037P	
PROJECT		023P			044P	
					532	P
0414 201104695 (A) N M SARVESH NARSINUHA PRABHU					** RESULT WITHHELD - (SEM. 5 NOT PASSED) **	
VLSI CIRCUIT DESIGN	035 $5	010	045E $5		027E	
ELECTRICAL POWER SYSTEM II	042	017	059E			
ADVANCED CONTROLLED DRIVES	000	004	004A			
NEURAL NETWORKS & FUZZY LOGIC	011	010	021F		030E	
FLEXIBLE AC TRANSMISSION SYSTEM	022	010	032F		037E	
PROJECT		015E			025E	
0415 201105573 (A) N M SHENVI KUDCHADKAR SHOUNAK VIVEK						
VLSI CIRCUIT DESIGN	070	019	089P		044P	
ELECTRICAL POWER SYSTEM II	048	020	068P			
ADVANCED CONTROLLED DRIVES	075	011	086P			
NEURAL NETWORKS & FUZZY LOGIC	061	017	078P		040P	
FLEXIBLE AC TRANSMISSION SYSTEM	060	013	073P		040P	
PROJECT		023P			045P	
					586	P
0416 201105574 (A) N M SHENVI SANGAVKAR AMEY DINESH						
VLSI CIRCUIT DESIGN	090	024	114P		048P	
ELECTRICAL POWER SYSTEM II	062	025	087P			
ADVANCED CONTROLLED DRIVES	077	011	088P			
NEURAL NETWORKS & FUZZY LOGIC	081	025	106P		048P	
FLEXIBLE AC TRANSMISSION SYSTEM	068	022	090P		046P	
PROJECT		024P			045P	
					696	P
0417 201104613 (A) N M SHET NARVEKAR NIHAL ALIAS ASHISH ANAND						
VLSI CIRCUIT DESIGN	073	020	093P		038P	
ELECTRICAL POWER SYSTEM II	068	020	088P			
ADVANCED CONTROLLED DRIVES	069	012	081P			
NEURAL NETWORKS & FUZZY LOGIC	062	013	075P		040P	
FLEXIBLE AC TRANSMISSION SYSTEM	050	011	061P		037P	
PROJECT		022P			042P	
					577 #09	P

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COLLEGEWISE STUDENT RESULT REGISTER FOR VII TH SEMESTER EXAM

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SEAT NO.	P.R. NO. ARE PRO CD. VI. / PAPER DESCRIPTION	THEORY	SESSIONAL	TOTAL	PRACTICAL	ORAL		REMARKS
		MAX. 100	25	125	50 OR 25	50 OR 25	MAX.850	
		MIN. 040		50	20 OR 10	20 OR 10	MIN.340	
0418	201105577 (A) N M SHIRODKAR SHUBHAM UMESH							
	VLSI CIRCUIT DESIGN	078	023	101P		044P		
	ELECTRICAL POWER SYSTEM II	074	023	097P				
	ADVANCED CONTROLLED DRIVES	066	011	077P				
	NEURAL NETWORKS & FUZZY LOGIC	064	020	084P		045P		
	FLEXIBLE AC TRANSMISSION SYSTEM	060	017	077P		044P		
	PROJECT		023P			045P		
							637	P
0419	201105578 (A) N M SHIRVOIKAR AJAY							
	VLSI CIRCUIT DESIGN	070	018	088P		030P		
	ELECTRICAL POWER SYSTEM II	053	012	065P				
	ADVANCED CONTROLLED DRIVES	054	006	060P				
	NEURAL NETWORKS & FUZZY LOGIC	053	011	064P		035P		
	FLEXIBLE AC TRANSMISSION SYSTEM	045	010	055P		038P		
	PROJECT		015P			032P		
							482	P
0420	201105579 (A) N F SHIRWAIKAR SIYA PRADEEP							
	VLSI CIRCUIT DESIGN	074	018	092P		042P		
	ELECTRICAL POWER SYSTEM II	046	022	068P				
	ADVANCED CONTROLLED DRIVES	083	014	097P				
	NEURAL NETWORKS & FUZZY LOGIC	066	021	087P		044P		
	FLEXIBLE AC TRANSMISSION SYSTEM	053	014	067P		040P		
	PROJECT		022P			042P		
							601	P
0421	201203884 (A) N M SMINAY DHARMAJI DESSAI							
	VLSI CIRCUIT DESIGN	052	016	068P		037P		
	ELECTRICAL POWER SYSTEM II	050	017	067P				
	ADVANCED CONTROLLED DRIVES	075	006	081P				
	NEURAL NETWORKS & FUZZY LOGIC	040	010	050P		038P		
	FLEXIBLE AC TRANSMISSION SYSTEM	039 #01	006	045P#05		038P		
	PROJECT		022P			045P		
							491 #12	P
0422	201104908 (A) N M SOURAV TRIVEDI							
	VLSI CIRCUIT DESIGN	062	019	081P		038P		
	ELECTRICAL POWER SYSTEM II	071	023	094P				
	ADVANCED CONTROLLED DRIVES	085	013	098P				
	NEURAL NETWORKS & FUZZY LOGIC	079	022	101P		044P		
	FLEXIBLE AC TRANSMISSION SYSTEM	067	022	089P		042P		
	PROJECT		020P			045P		
							652	P

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SEAT NO. / P.R. NO. / PAPER CD. / DESCRIPTION		THEORY		SESSIONAL	TOTAL		PRACTICAL	ORAL		REMARKS
	MAX.	100		25	125		50 OR 25	50 OR 25	MAX.850	
	MIN.	040			50		20 OR 10	20 OR 10	MIN.340	
0423 201104899 (A) N F TRISILA SINGH RAJPUT										
VLSI CIRCUIT DESIGN		070		023	093P			040P		
ELECTRICAL POWER SYSTEM II		064		024	088P					
ADVANCED CONTROLLED DRIVES		083		011	094P					
NEURAL NETWORKS & FUZZY LOGIC		085		023	108P			044P		
FLEXIBLE AC TRANSMISSION SYSTEM		067		021	088P			045P		
PROJECT				022P				041P		
									663	P
0424 201105582 (A) N M VARGHESE SANCHU							** RESULT WITHHELD - (SEM. 6 NOT PASSED) **			
VLSI CIRCUIT DESIGN		063		017	080E			038E		
ELECTRICAL POWER SYSTEM II		056		016	072E					
ADVANCED CONTROLLED DRIVES		022		003	025F					
NEURAL NETWORKS & FUZZY LOGIC		060		014	074E			038E		
FLEXIBLE AC TRANSMISSION SYSTEM		045		010	055E			038E		
PROJECT				022E				042E		
0425 201104714 (A) N M VIJAYKUMAR VISHAL										
VLSI CIRCUIT DESIGN		047		013	060P			040P		
ELECTRICAL POWER SYSTEM II		042		022	064P					
ADVANCED CONTROLLED DRIVES		054		006	060P					
NEURAL NETWORKS & FUZZY LOGIC		065		013	078P			038P		
FLEXIBLE AC TRANSMISSION SYSTEM		042		012	054P			040P		
PROJECT				020P				045P		
									499	P
0426 200703655 (E) N M CHAGAS PEREIRA JASON MARK STEFAN							** RESULT WITHHELD - (SEM. 6 NOT PASSED) **			
VLSI CIRCUIT DESIGN		048		010	058+			030+		
ELECTRICAL POWER SYSTEM II		038	$2	011	049+	$2				
ADVANCED CONTROLLED DRIVES		036	$4	010	046+	$4				
NEURAL NETWORKS & FUZZY LOGIC		040		012	052P			030+		
FLEXIBLE AC TRANSMISSION SYSTEM		044		007	051+			033+		
PROJECT				020+				038+		
0427 200907816 (E) N M CHARI SAGAR DATTA										
VLSI CIRCUIT DESIGN		042		010	052+			036+		
ELECTRICAL POWER SYSTEM II		054		010	064+					
ADVANCED CONTROLLED DRIVES		050		010	060+					
NEURAL NETWORKS & FUZZY LOGIC		040		001	041F			025+		
FLEXIBLE AC TRANSMISSION SYSTEM		046		008	054+			029+		
PROJECT				020+				045+		
									426	F

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EXAM YEAR : NOV-2014

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			VI.			MAX. 100	25	125	50 OR 25	50 OR 25	MAX.850
						MIN. 040		50	20 OR 10	20 OR 10	MIN.340
0428	200908072	(E)	N	M	KHAIDEM KASISH KUMAR SINGH						** RESULT WITHHELD - (SEM. 6 NOT PASSED) **
	VLSI CIRCUIT DESIGN					046	010	056+		035+	
	ELECTRICAL POWER SYSTEM II					052	009	061+			
	ADVANCED CONTROLLED DRIVES					054	010	064+			
	NEURAL NETWORKS & FUZZY LOGIC					040	005	045P	@5	025+	
	FLEXIBLE AC TRANSMISSION SYSTEM					048	011	059+		025+	
	PROJECT						018+			037+	
0429	201008360	(E)	N	M	LOPES FRANCIS BOMDIN						** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
	VLSI CIRCUIT DESIGN					009	006	015F		035+	
	ELECTRICAL POWER SYSTEM II					052	016	068+			
	ADVANCED CONTROLLED DRIVES					067	010	077+			
	NEURAL NETWORKS & FUZZY LOGIC					045	015	060+		022+	
	FLEXIBLE AC TRANSMISSION SYSTEM					056	014	070+		037+	
	PROJECT						020+			044+	

READ BY CHECKED BY 16 MAR 2015 DECLARED ON ASST. REGISTRAR-E(PROF) Macedo 16/3/15 CONTROLLER OF EXAMINATIONS REGISTRAR

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